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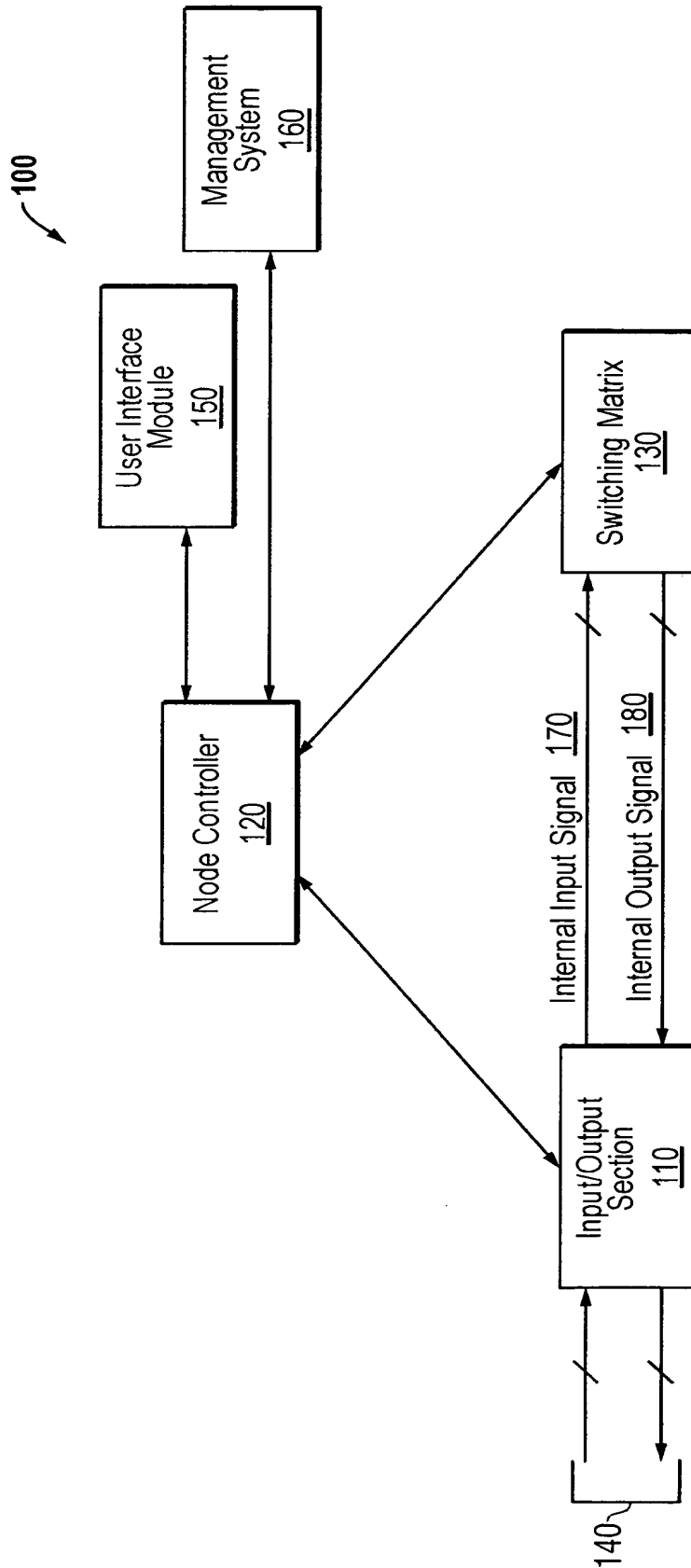
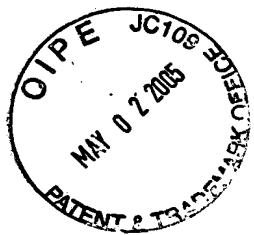


FIG. 1A



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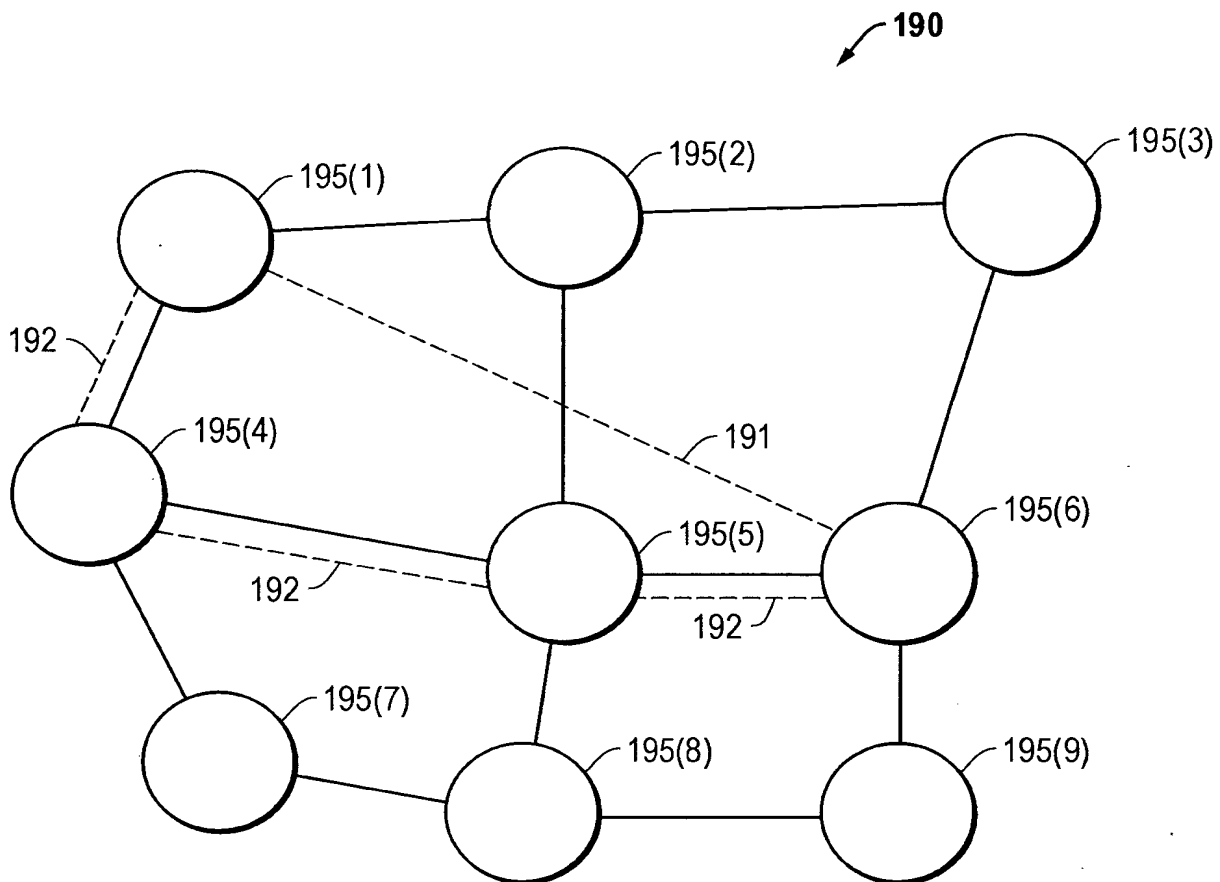
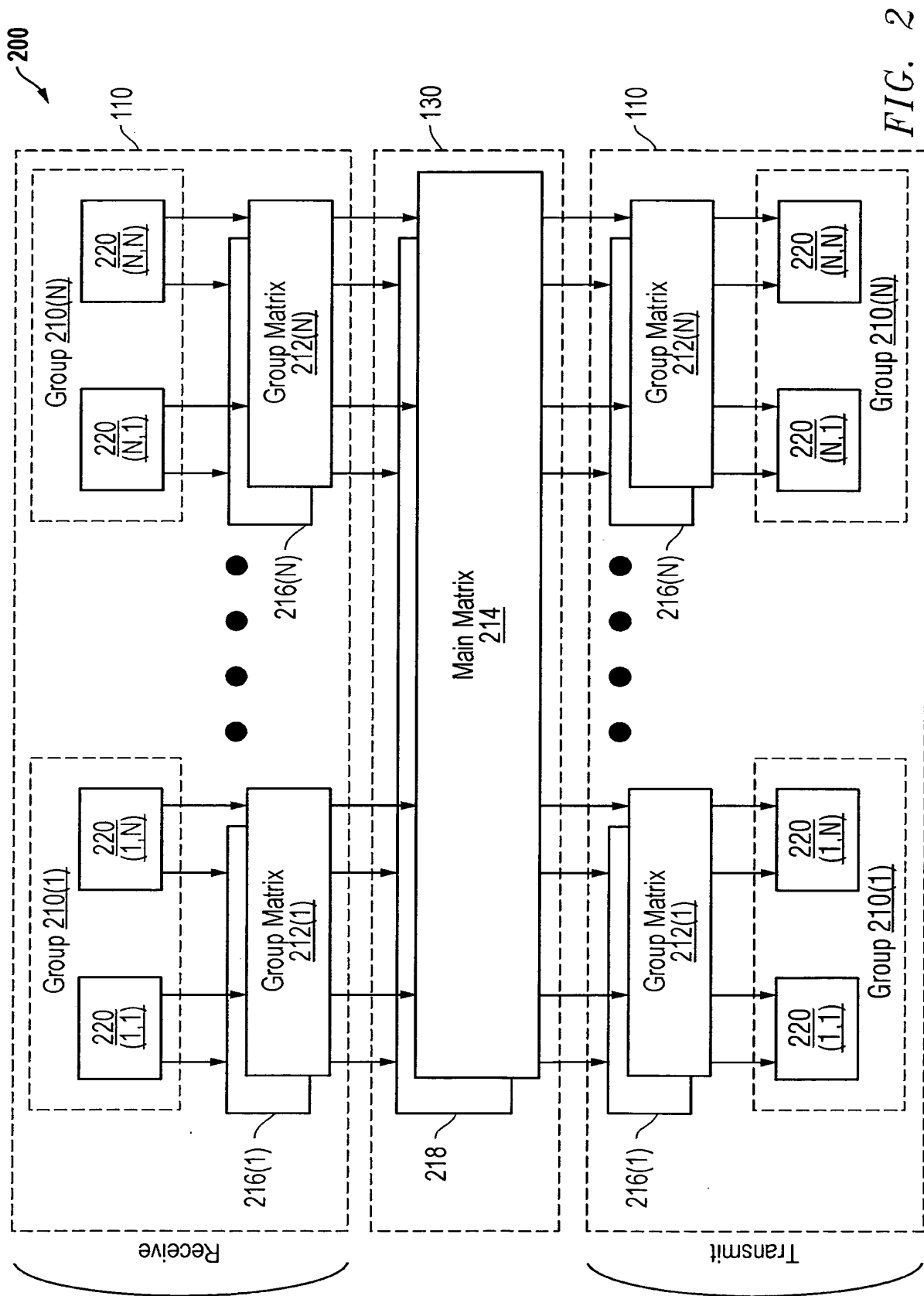
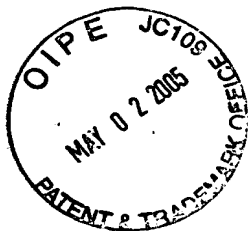


FIG. 1B



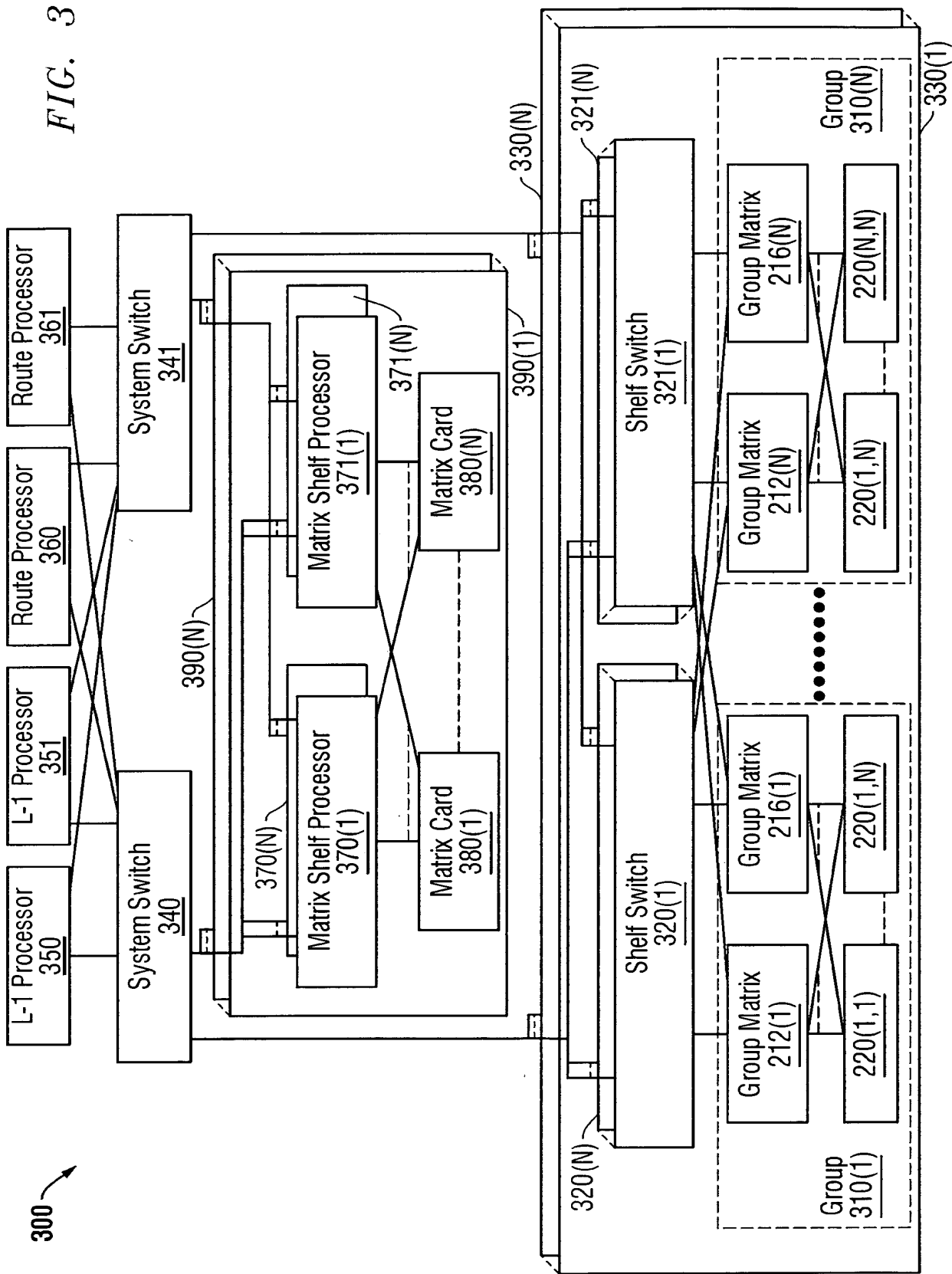
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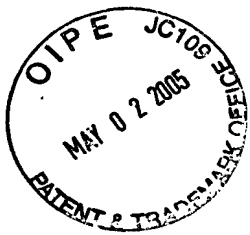




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FIG. 3





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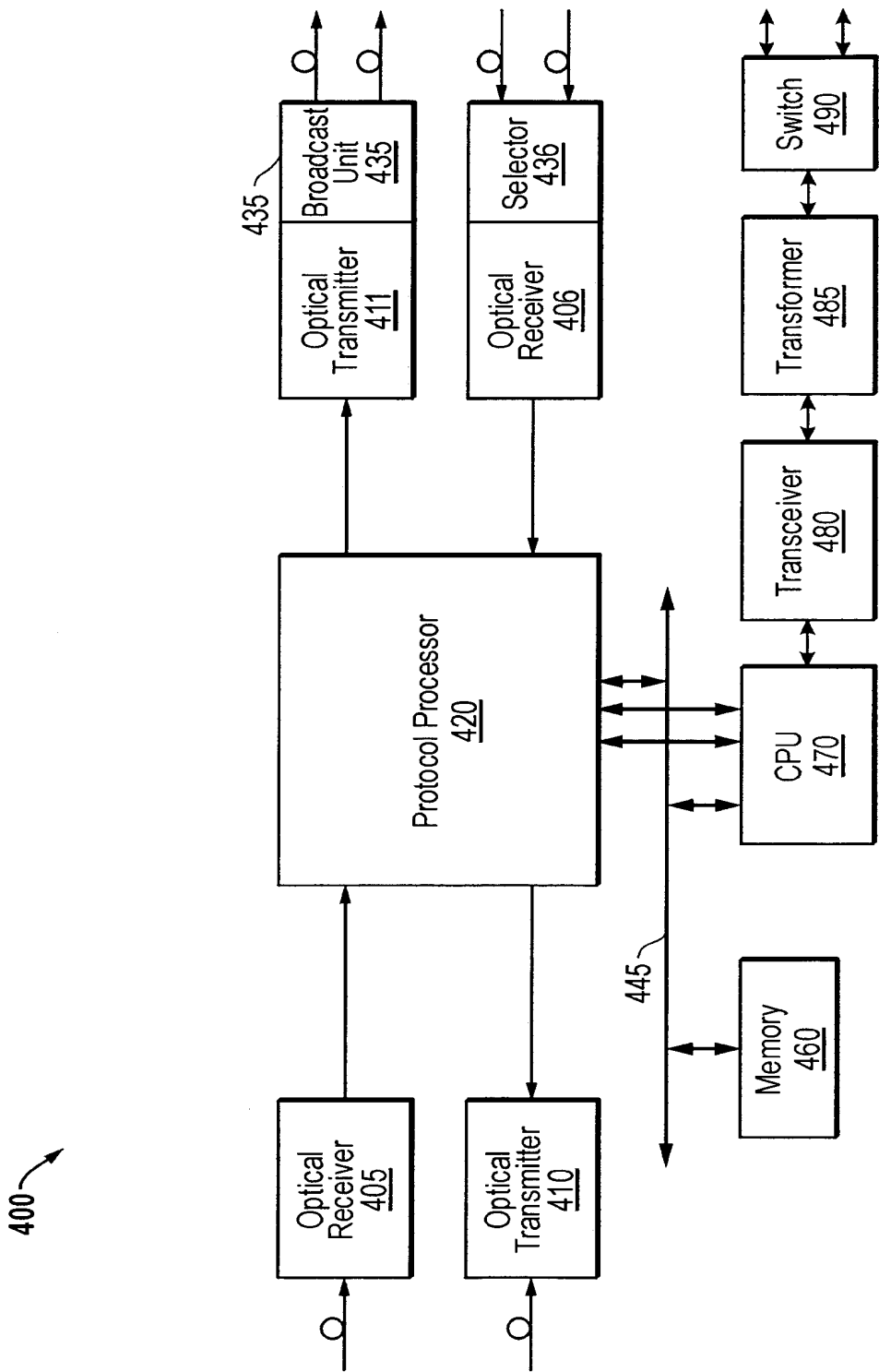


FIG. 4



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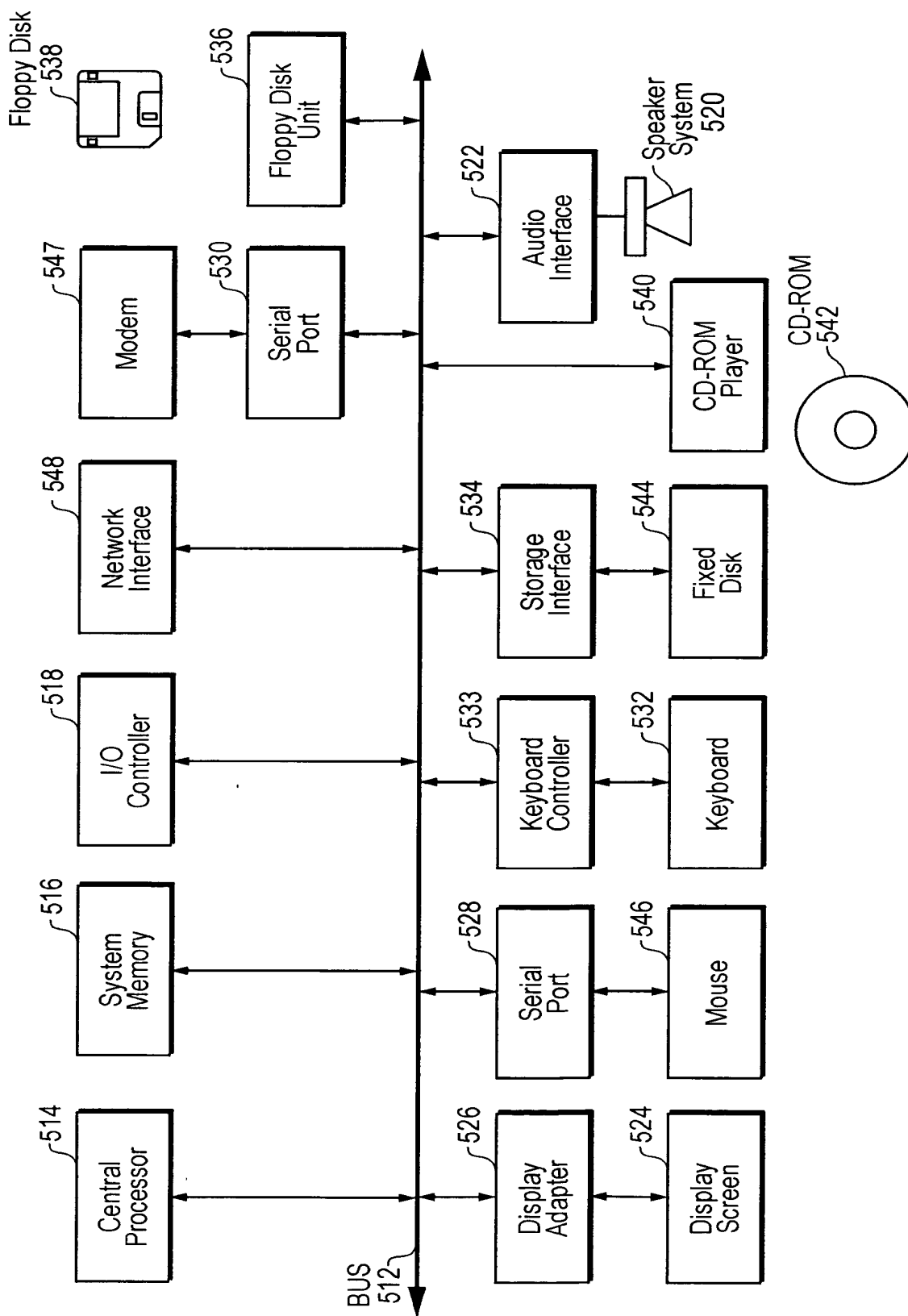


FIG. 5



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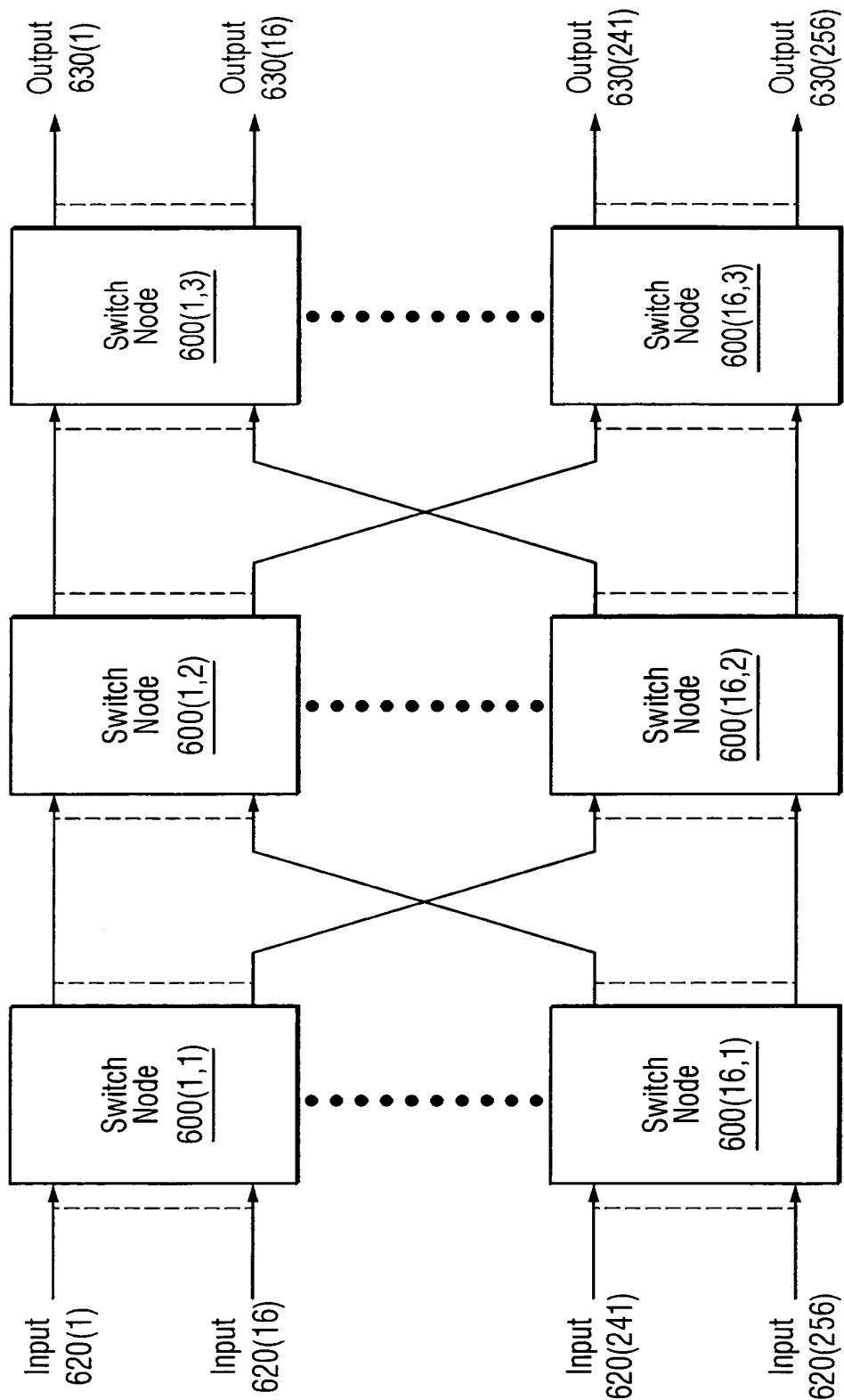
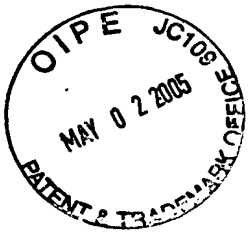


FIG. 6



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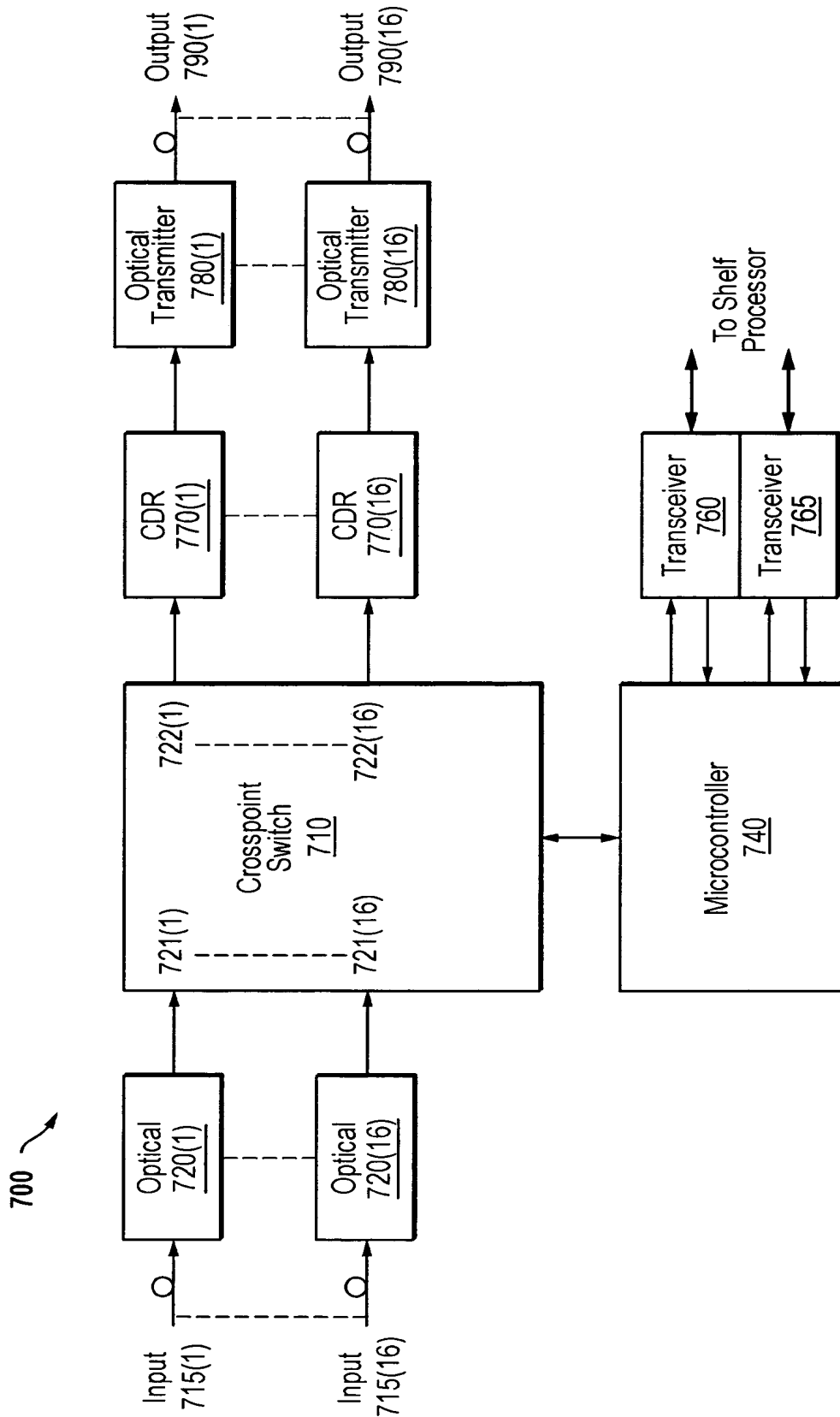
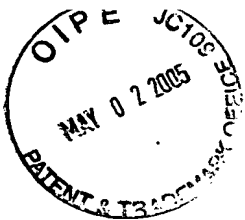
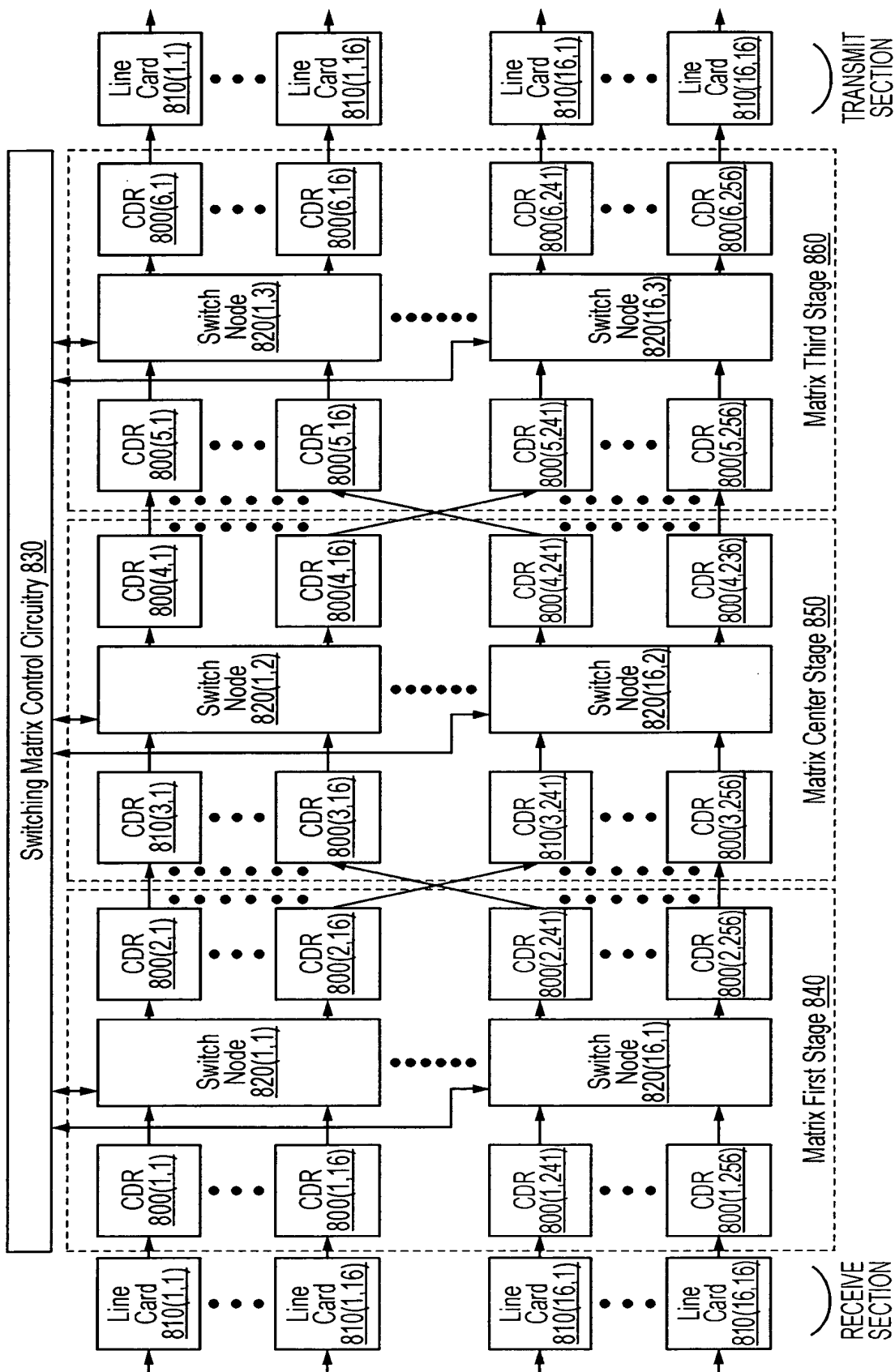


FIG. 7





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Attorney Docket No.: CIS0009P2US  
First Inventor: Douglas E. Duschatko  
Title: METHOD AND APPARATUS FOR DETECTING  
ERRORS IN A BACKPLANE FRAME

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SONET  
FRAME  
900  
↙

A1 902 U	A2 904 U	J0/Z0 906 U	Payload Bytes 990
B1 910 U	E1 912 U	F1 914 U	Payload Bytes 991
D1 920 U	D2 922 U	D3 924 U	Payload Bytes 992
H1 930 U	H2 932 U	H3 934 U	H4 936 U
B2 940 U	K1 942 U	K2 944 U	Payload Bytes 994
D4 950 U	D5 951 U	D6 952 U	Payload Bytes 995
D7 953 U	D8 954 U	D9 955 U	Payload Bytes 996
D10 956 U	D11 957 U	D12 958 U	Payload Bytes 997
S1/Z1 970 U	M1/Z2 972 U	E2 974 U	Payload Bytes 998

FIG. 9  
(Prior Art)

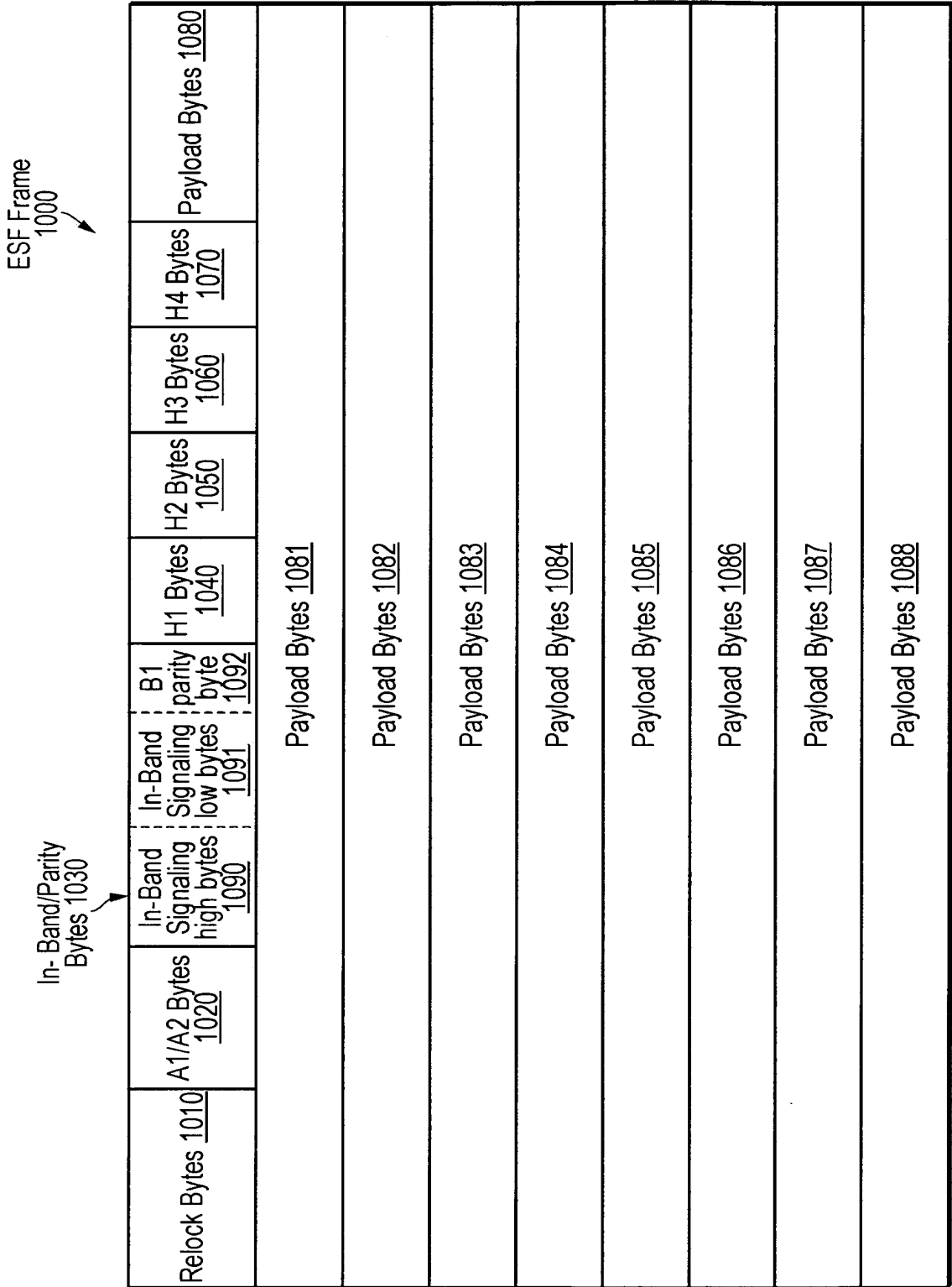
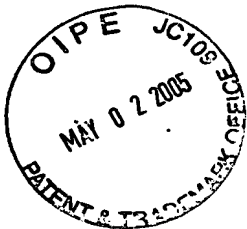
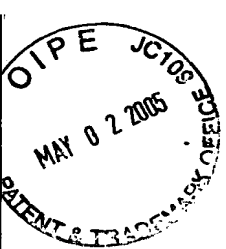
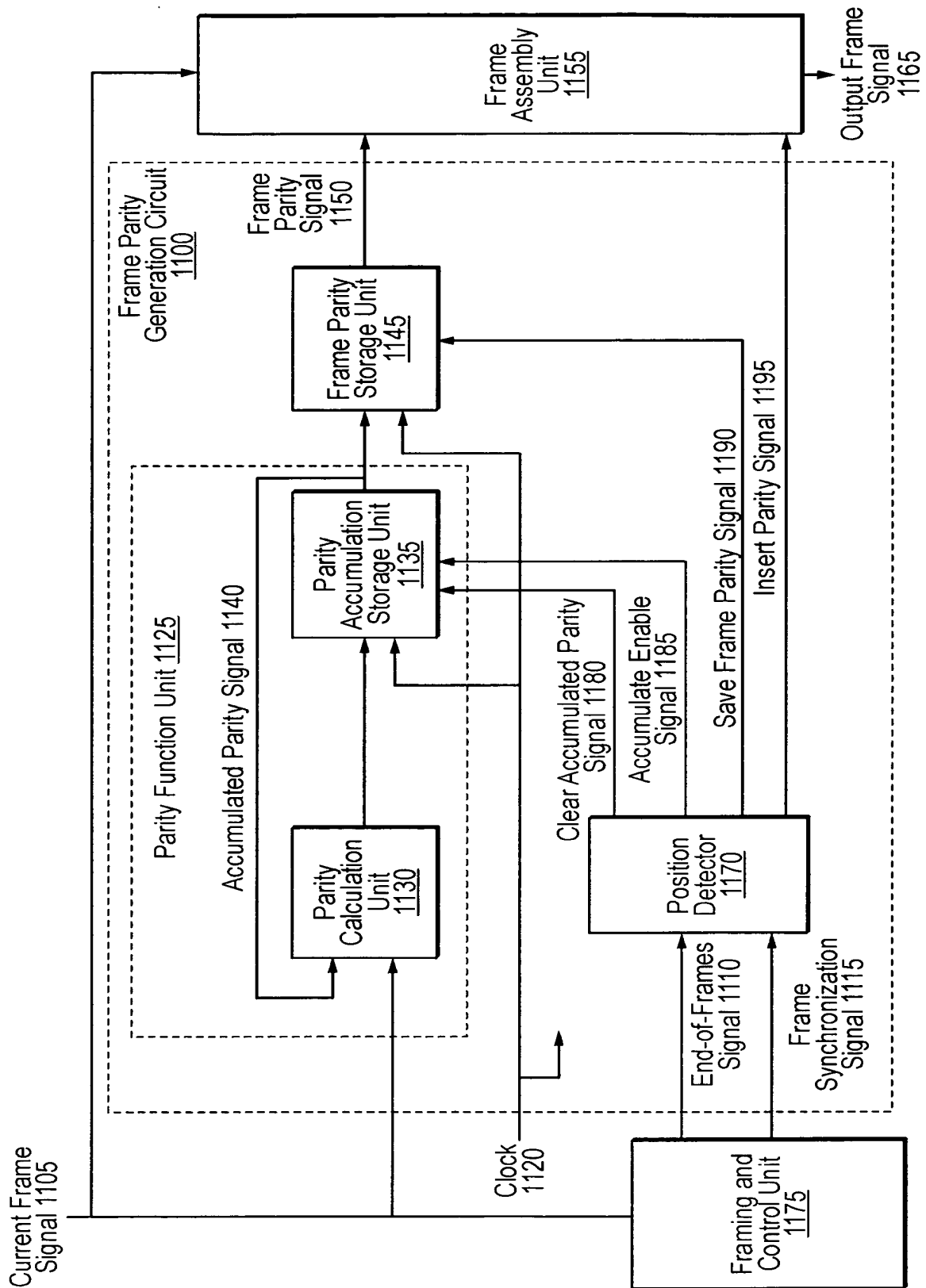


FIG. 10



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FIG. 11



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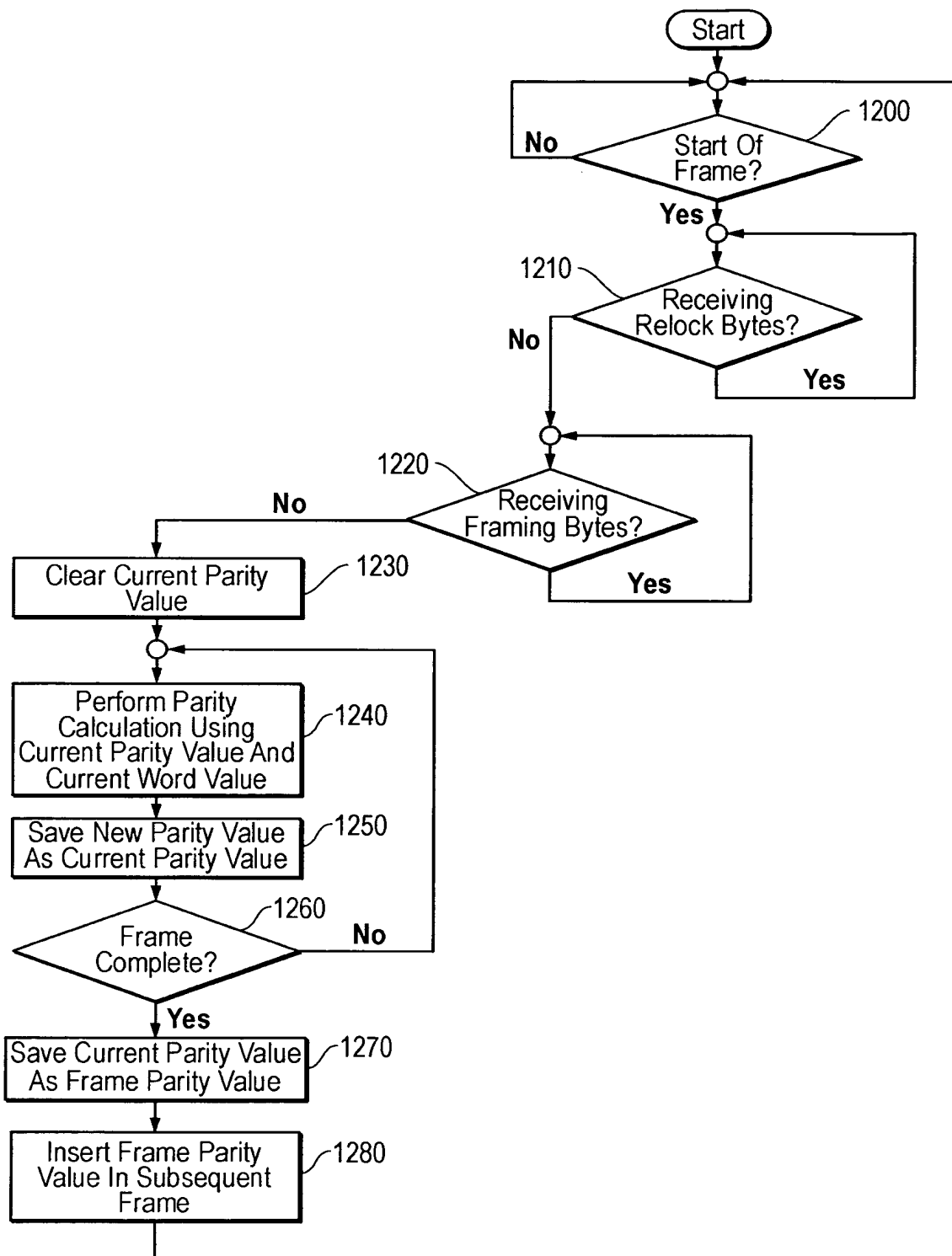


FIG. 12



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Frame Parity  
Checking Circuit  
1300

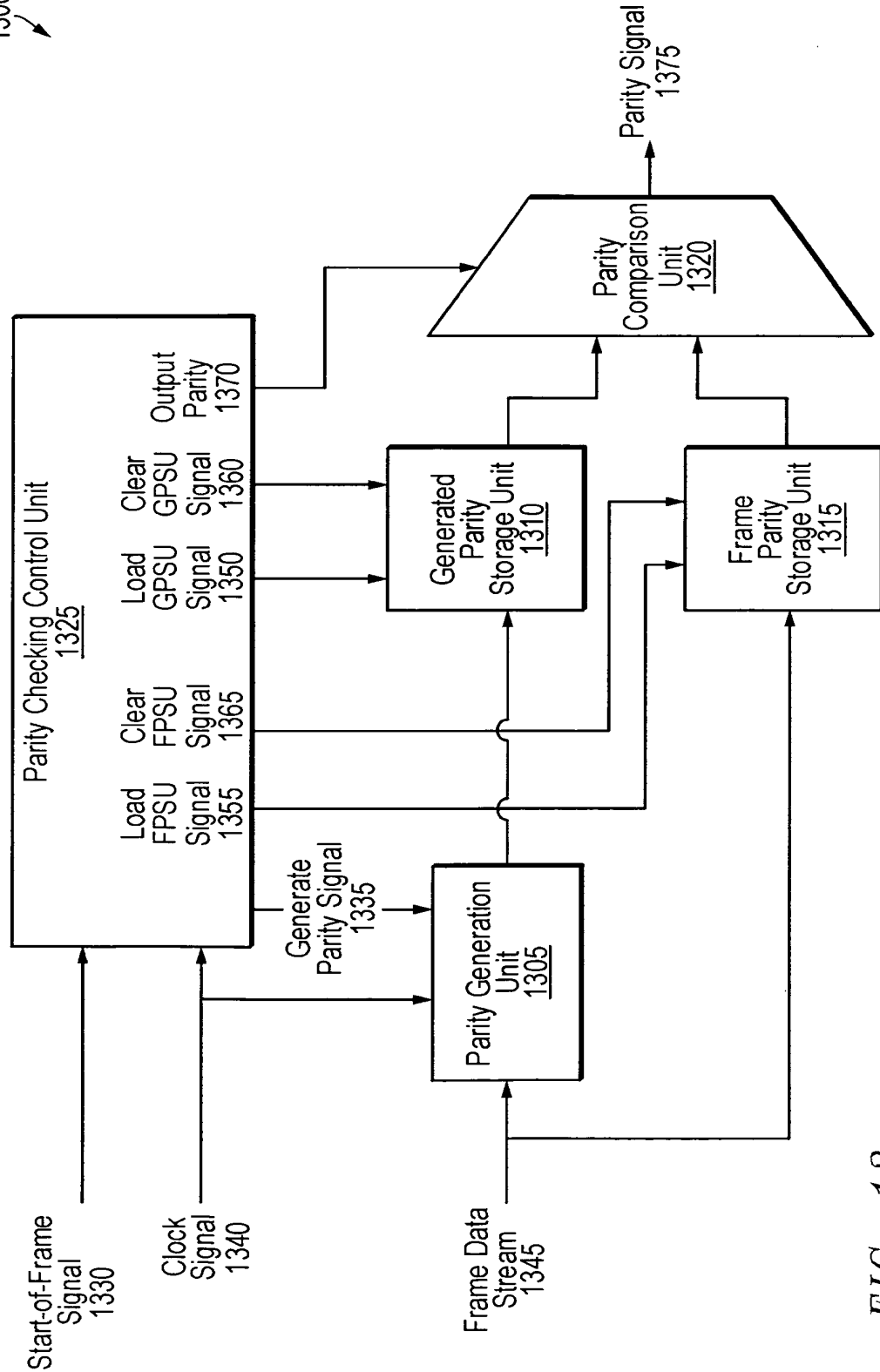


FIG. 13



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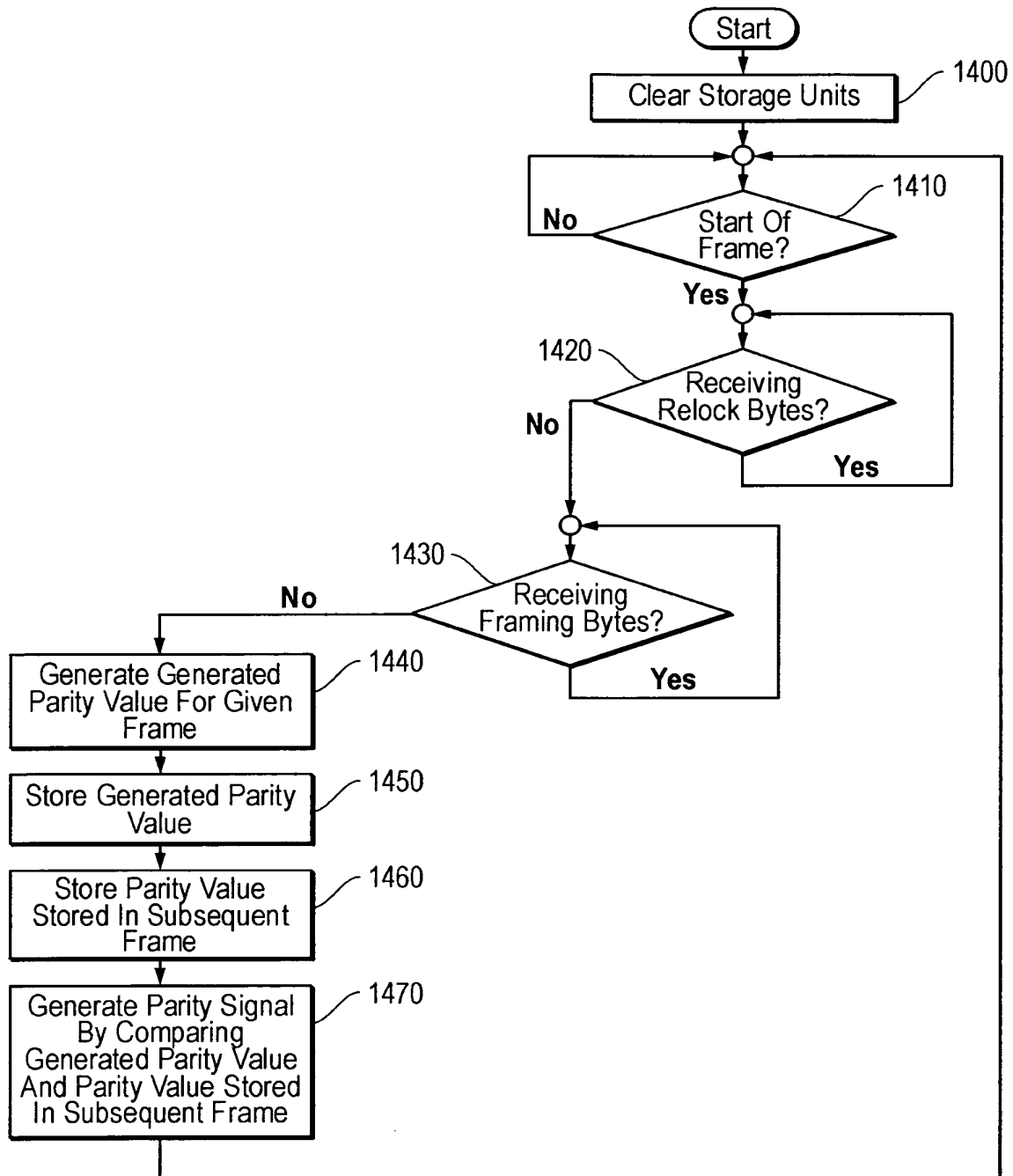


FIG. 14